

Notice of References Cited	Application/Control No. 09/685,272	Applicant(s)/Patent Under Reexamination DOWLING, ERIC M.	
	Examiner Tuan A Vu	Art Unit 2124	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,182,206	01-2001	Baxter, Michael A.	712/43
	C	US-5,247,627	09-1993	Murakami et al.	712/236
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NON-PATENT DOCUMENTS

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	U	Sudarsanam et al., "Analysis and Evaluation of Address Arithmetic Capabilities in Custom DSP Architectures", June 1997, Design Automation Conference, pp. 287-292.
	V	Stan Liao et al., "Storage Assignment to Decrease Code Size", June 1995, In Proceedings of ACM SIGPLAN'95, Conference on Programming Language Design and Implementation, Vol. 30, Issue 6, pp. 235—253
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.